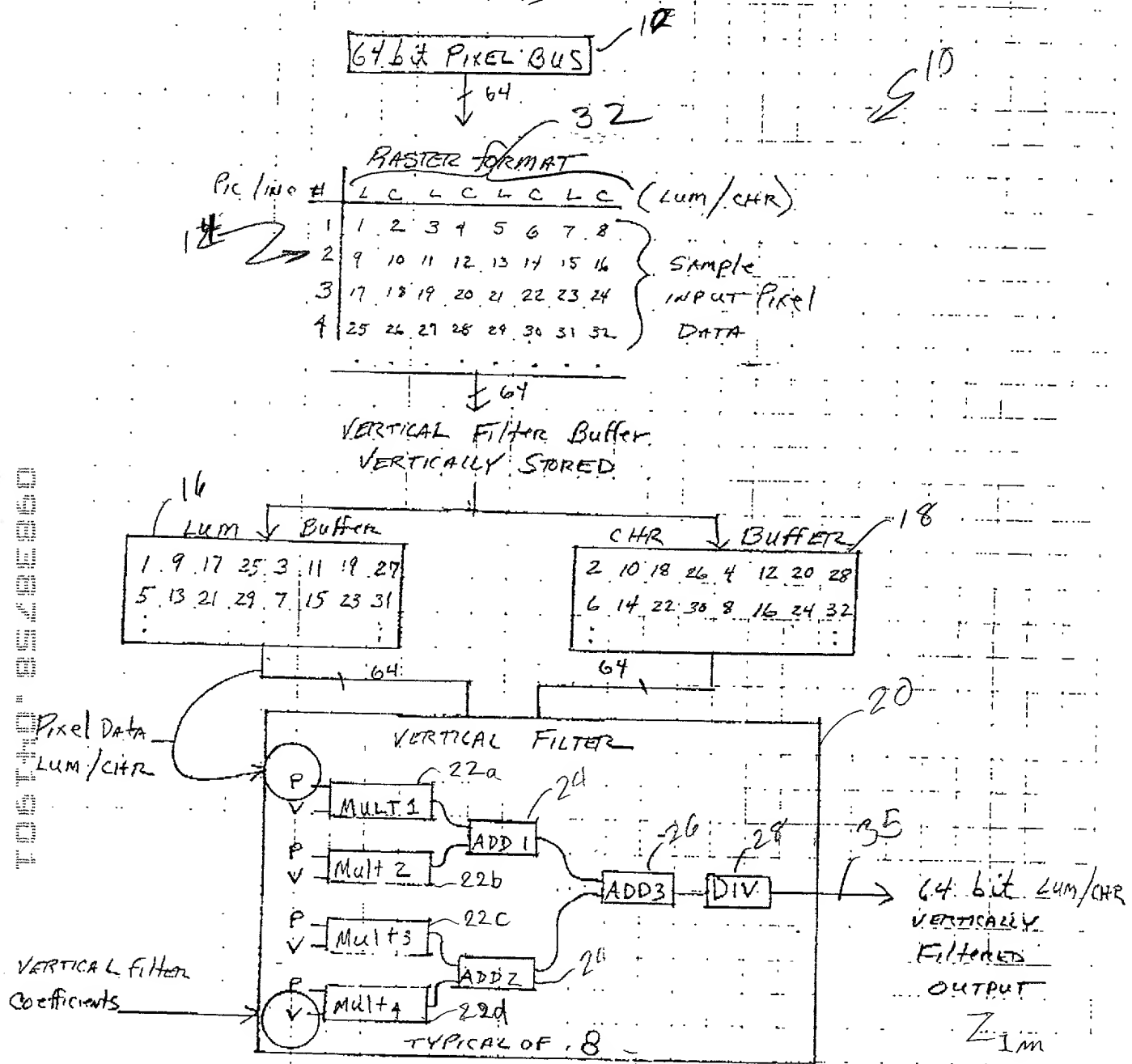


FIGURE 1. SCHEMATIC BLOCK DIAGRAM OF A
VERTICAL SPATIAL FILTERING CIRCUIT.
(RELATED ART)



$$Z_{im} = P_{rc} V_i + P_{rc} V_i + P_{rc} V_i + P_{rc} V_i / N$$

FIGURE 2 SCHEMATIC BLOCK DIAGRAM OF A
VERTICAL CHROMA CONVERSION CIRCUIT.
(RELATED ART)

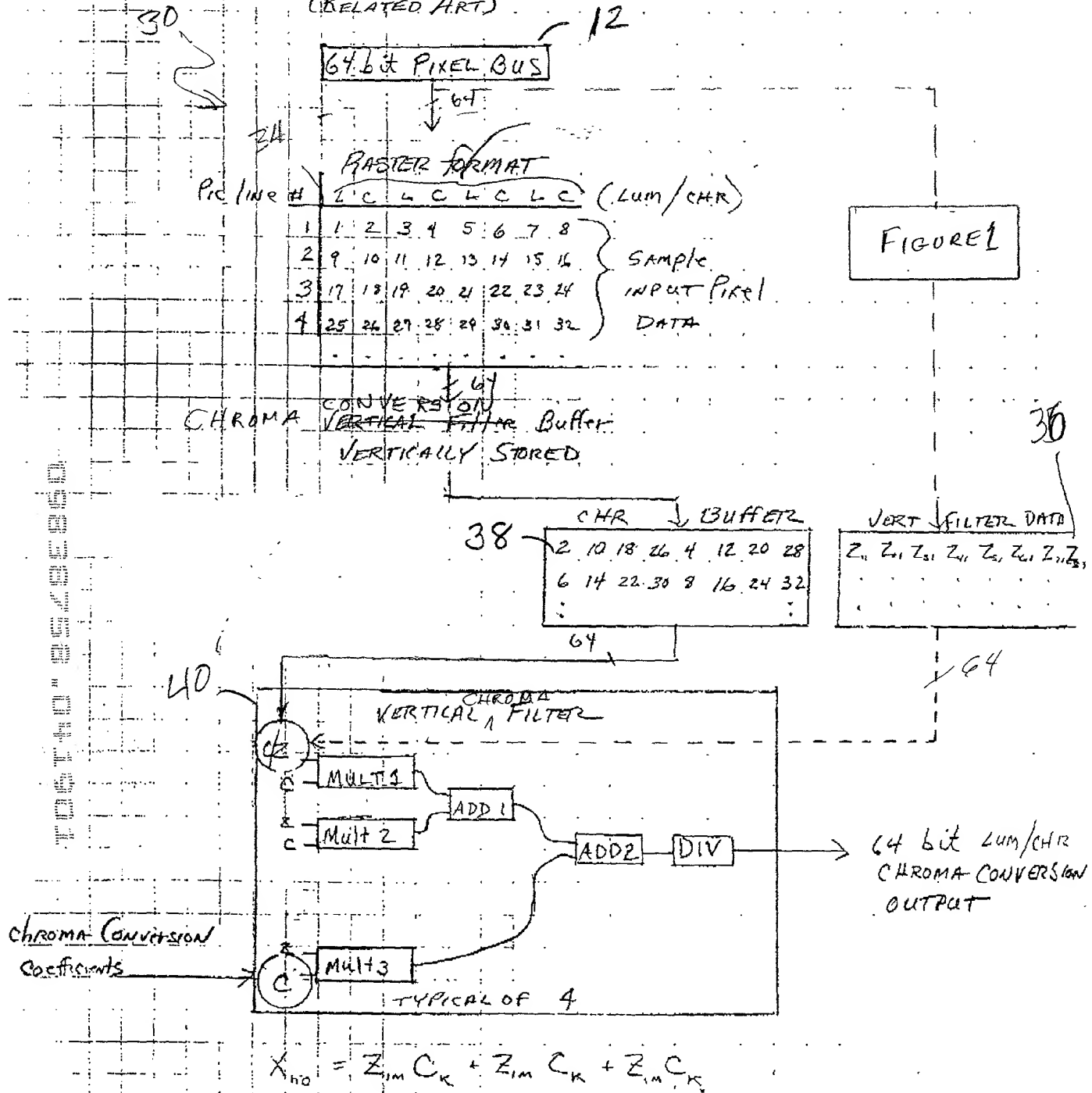
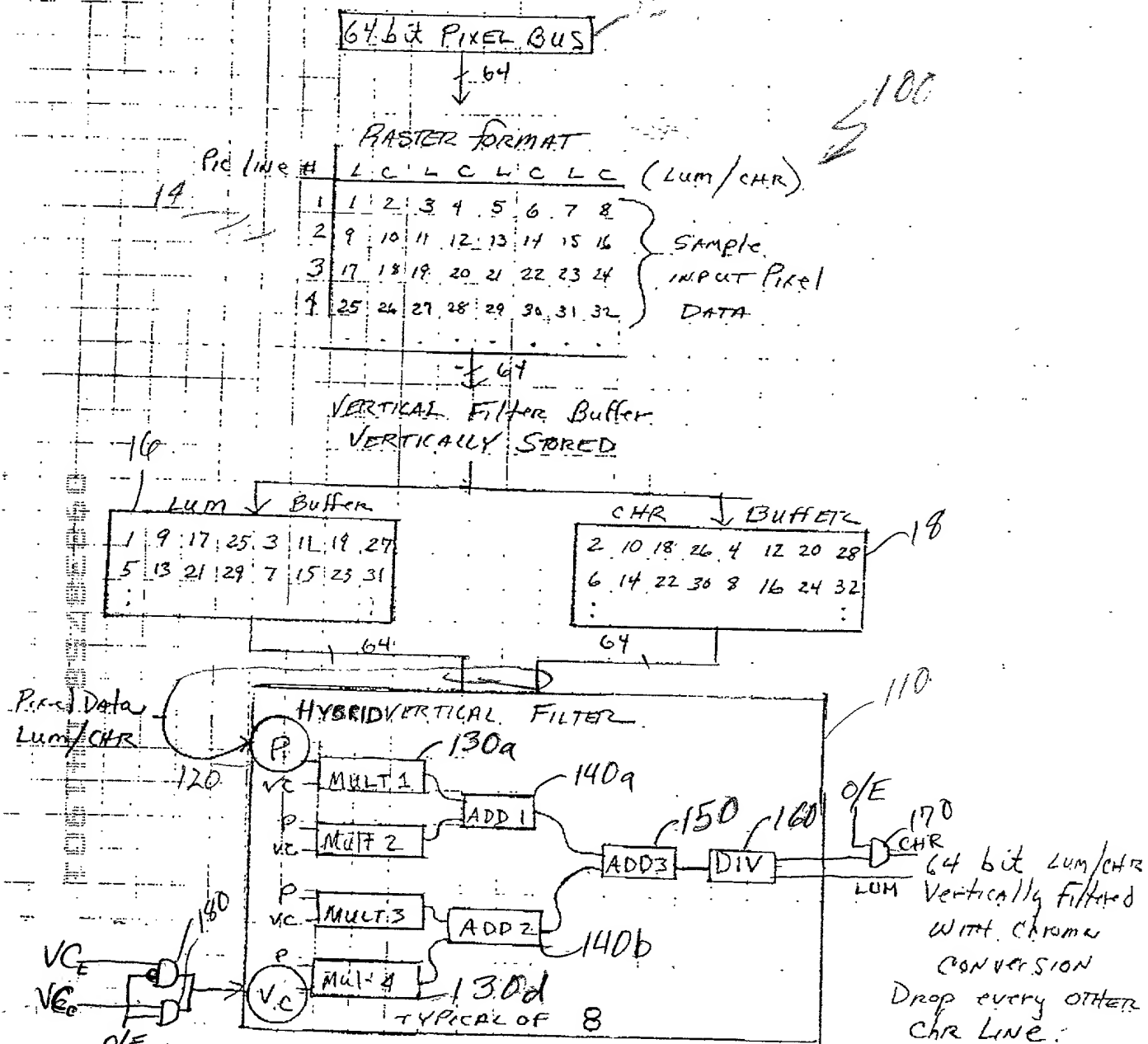


FIGURE 3 SCHEMATIC BLOCK DIAGRAM OF A
VERTICAL FILTER/CHROMA CONVERSION CIRCUIT



$$V_{10} = P_{RE} V_{C_K} + P(V_C + V_C) + P(V_C + V_C + V_C) + P(V_C + V_C + V_C) + P(V_C + V_C) + P V_C$$